

FIG. 1 PRIOR ART

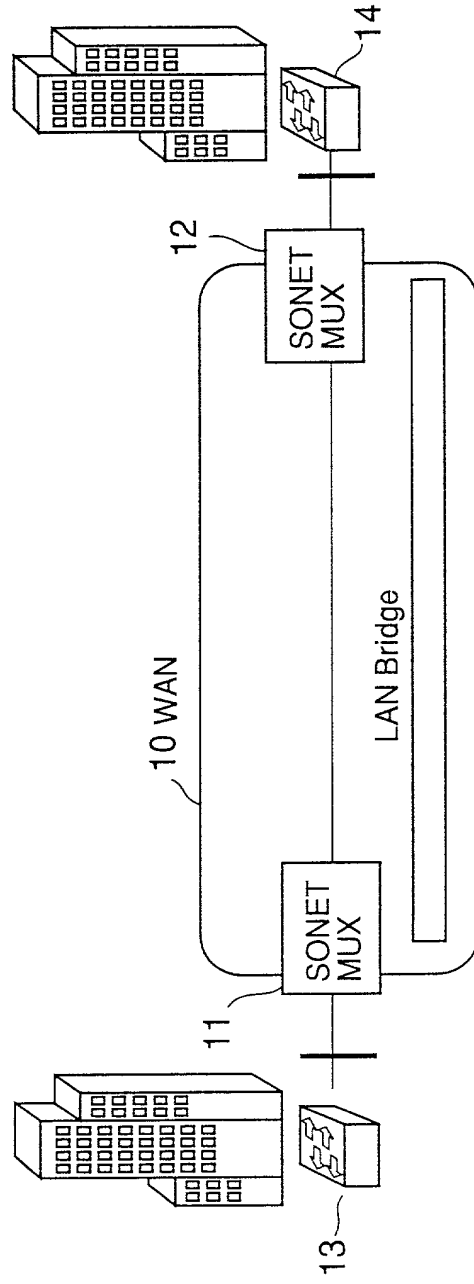


FIG. 2 PRIOR ART

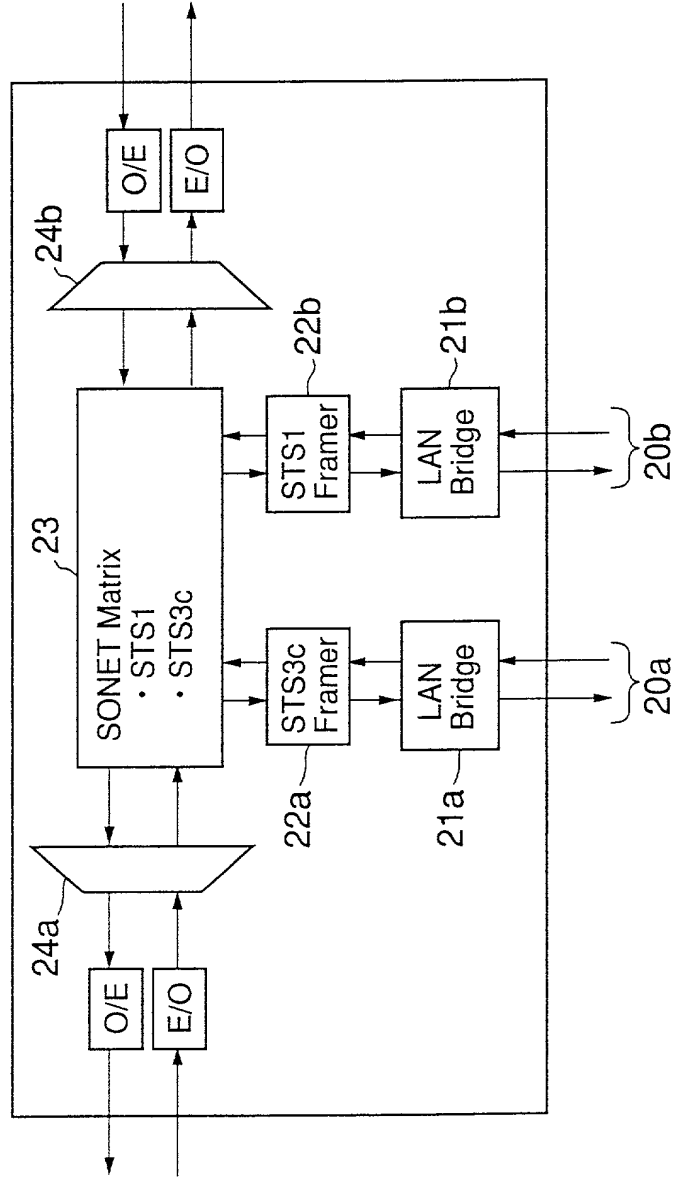


FIG. 3 PRIOR ART

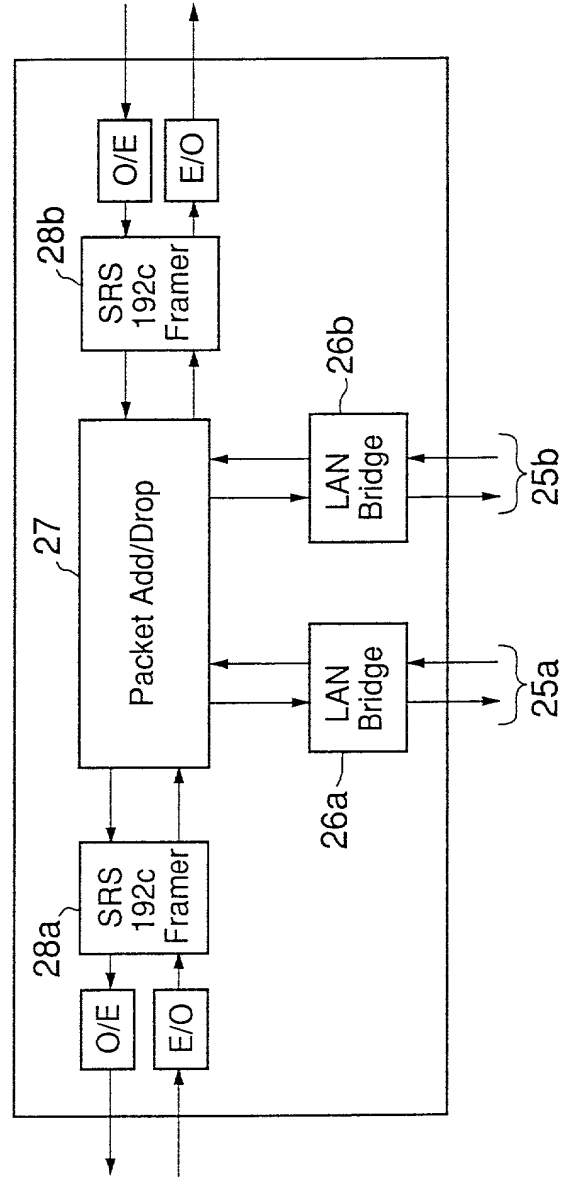


FIG. 4 PRIOR ART

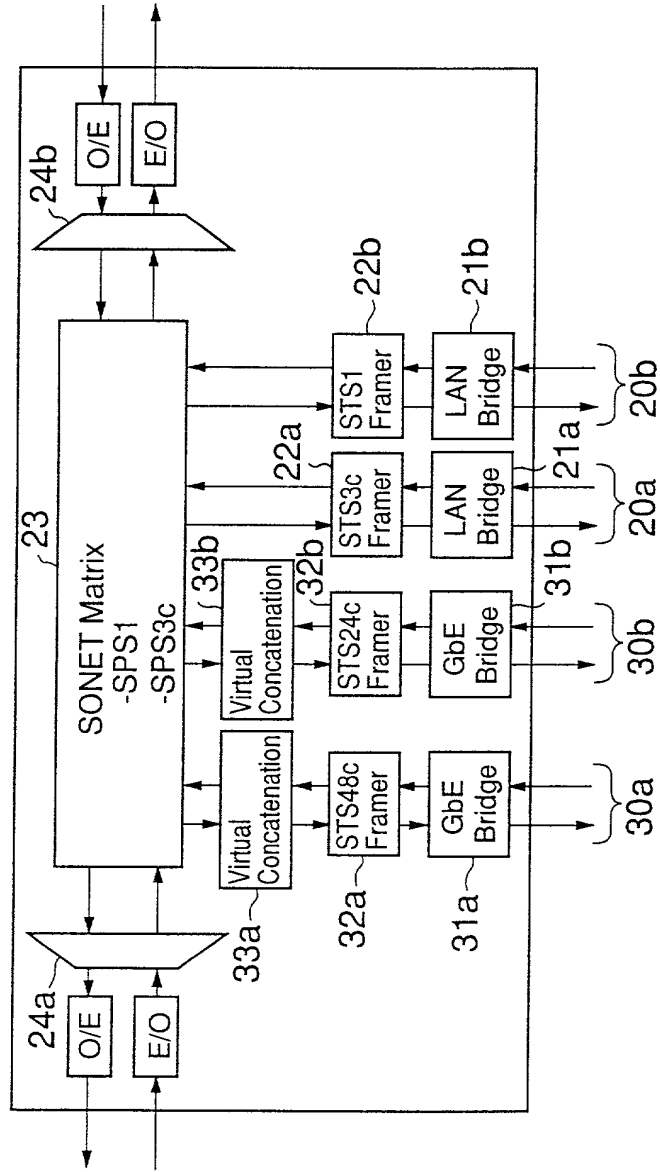


FIG. 5

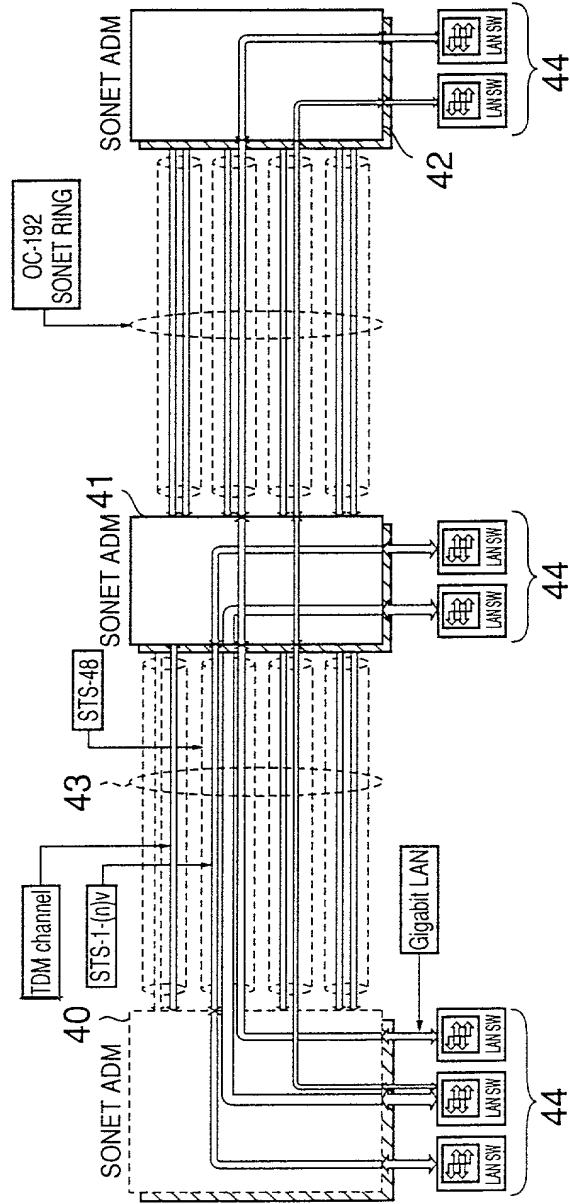


FIG. 6

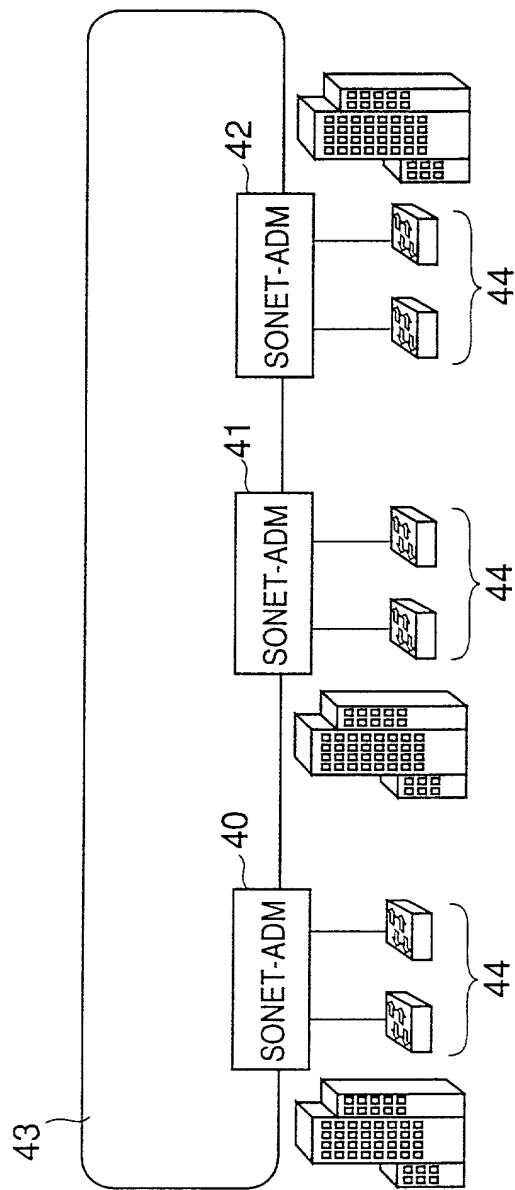


FIG. 7

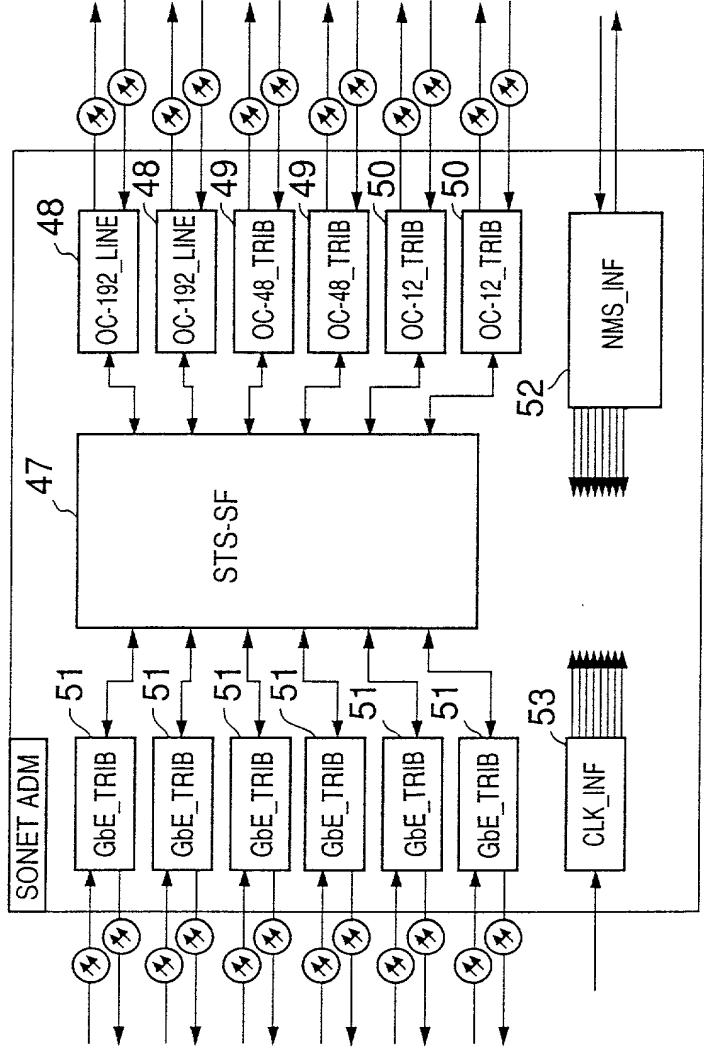


FIG. 8

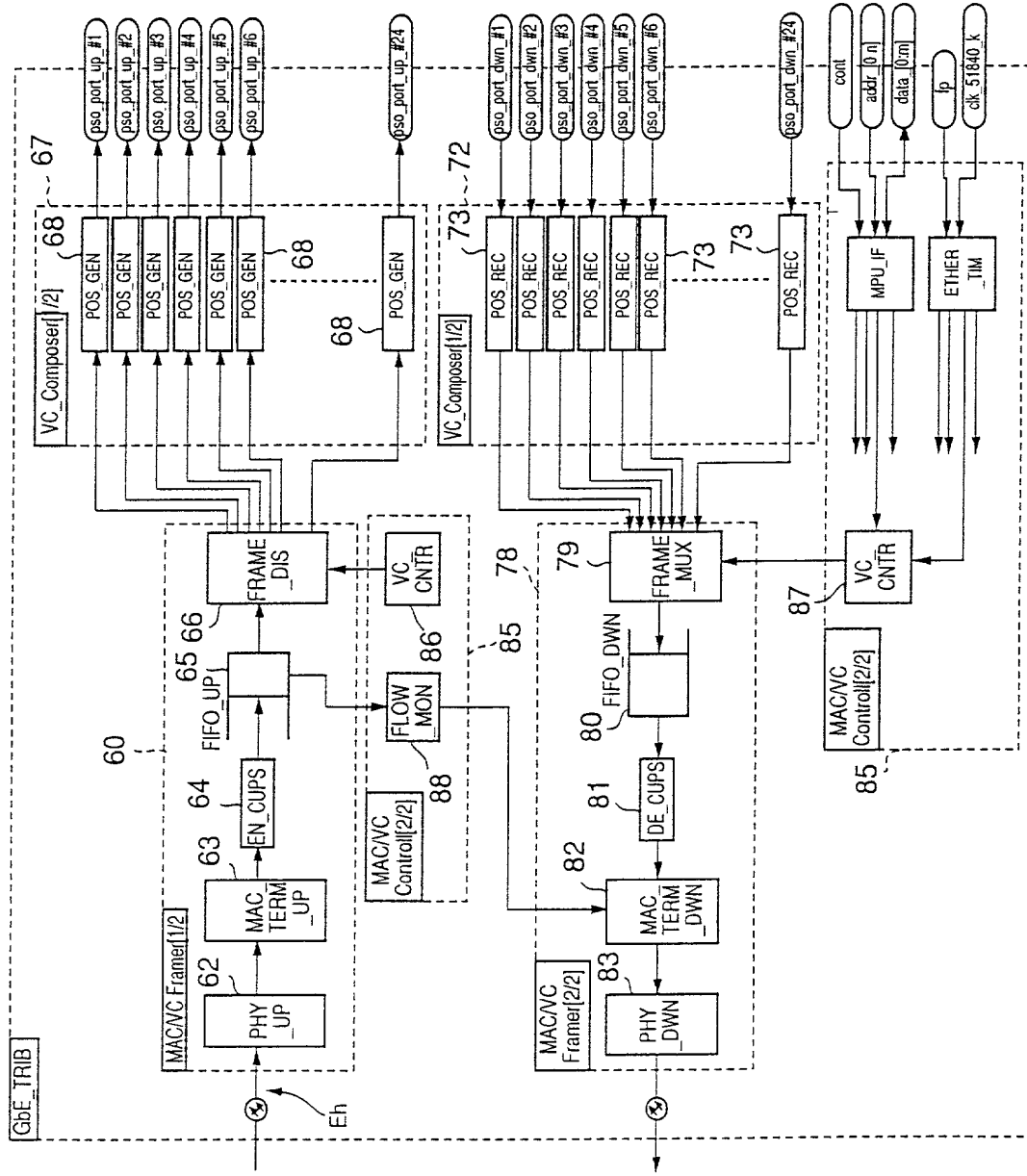


FIG. 9A

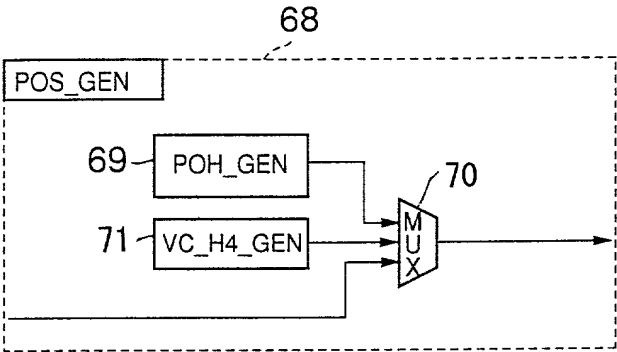


FIG. 9B

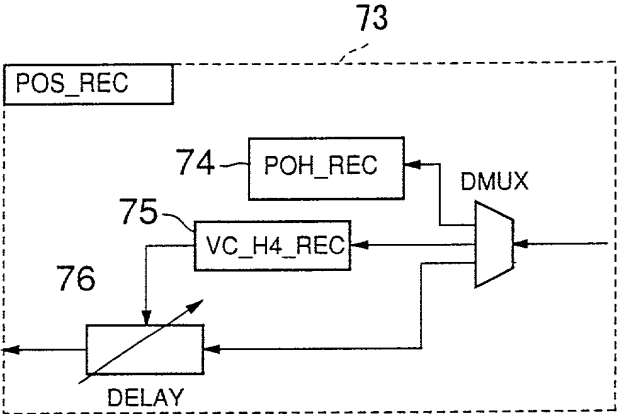


FIG. 10A

FIFO UP
Memory contents

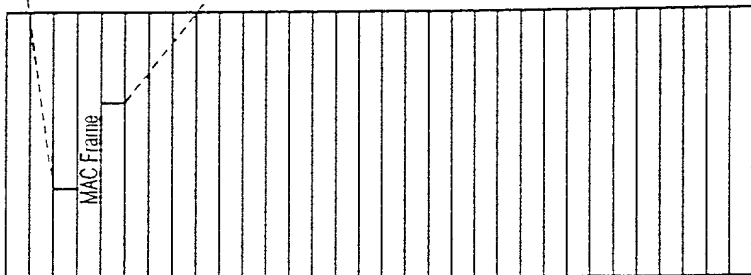


FIG. 10B

IEEE 802.3 MAC

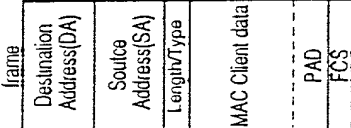


FIG. 10C

IEEE 802.3 MAC

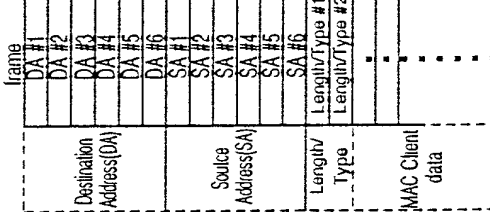


FIG. 10D

VC Composer

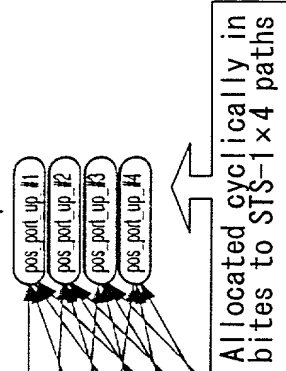


FIG. 12

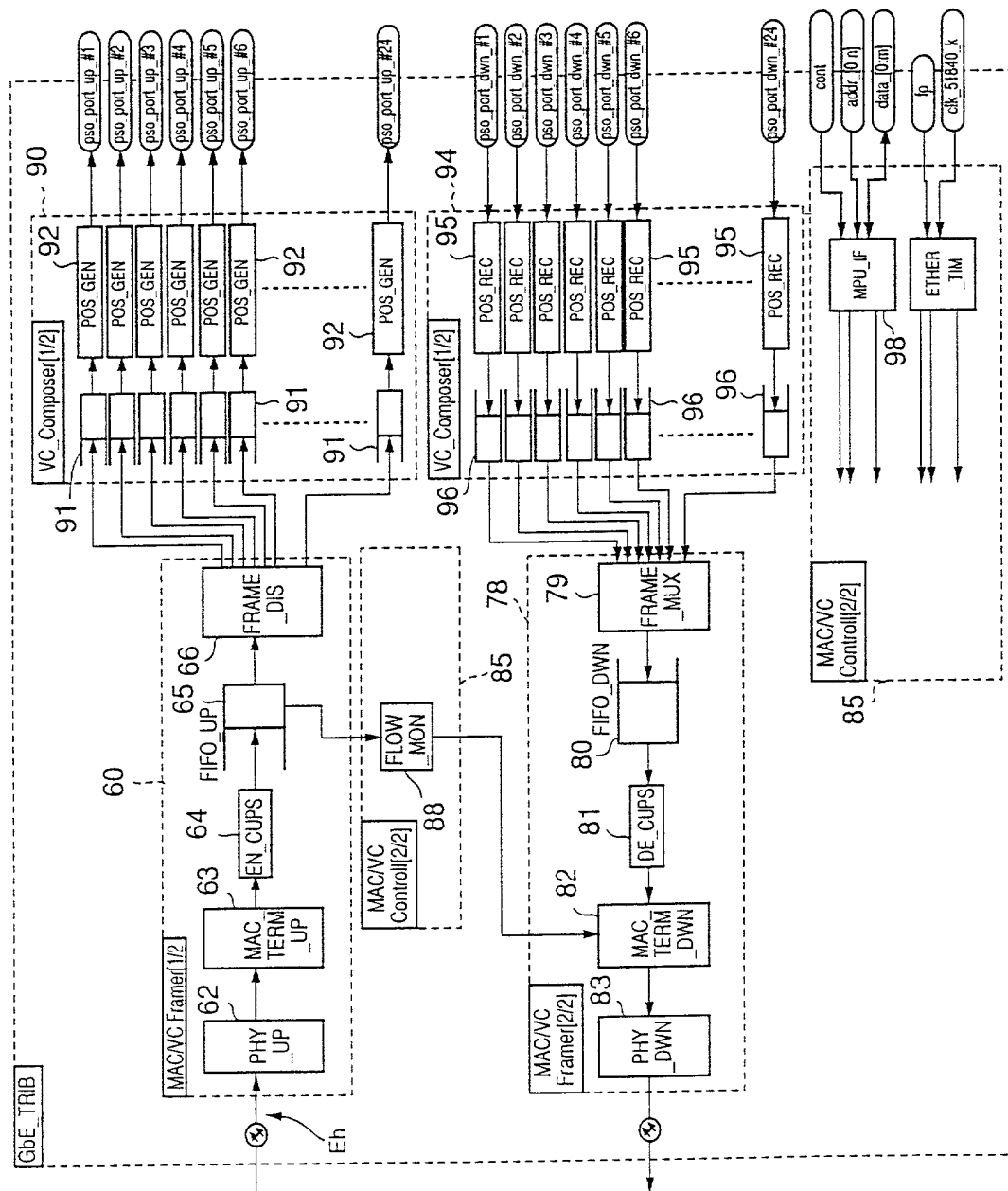


FIG. 14A

FIFO UP
Memory contents

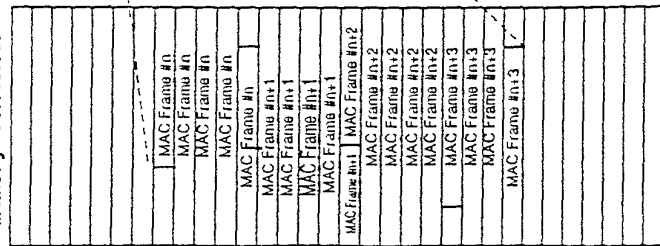


FIG. 14B

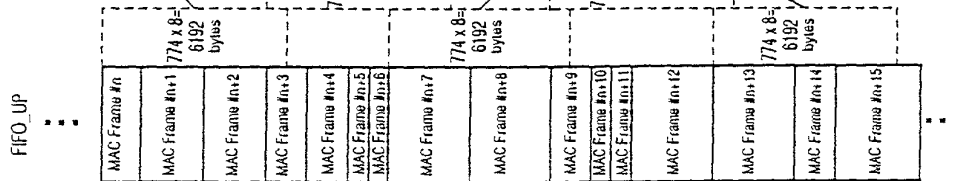


FIG. 14C

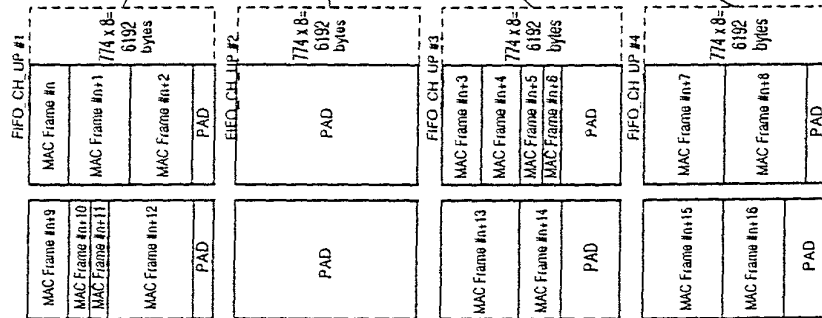
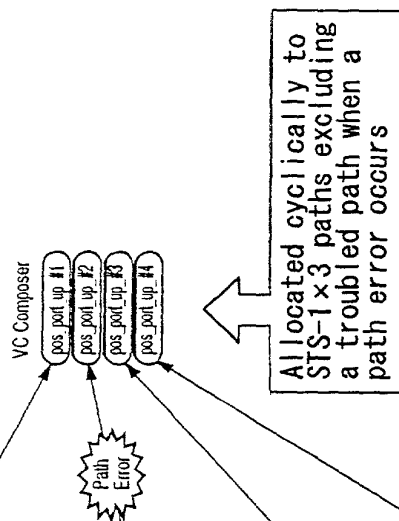


FIG. 14E



Allocated to a FIFO of each channel in units corresponding to 1 msec and a MAC frame (a packet); 1 msec corresponds to 8 STS frames ($774 \times 8 = 6192$ bytes)

FIG.15

